



Intelop announces their second generation customizable TCP-Offload Engine at 2-Gbps with integrated Ethernet MAC and AMBA host bus Silicon IP for SOC, ASIC and FPGA customers in Network equipment, Network Security, Telecom, SAN/NAS markets

Customers will be able to customize this TOE engine by adding differentiable features and integrate this synthesizable core in the design of SOCs/ASICs/FPGAs to develop enhanced differentiable products.

Integrated TOE+GEMAC+AHB silicon IP with customizable features provides enhanced functionality in Layer-2-5 Switches/Routers, IPS/IDS appliances and Network Security appliances, Servers and NICs. Advanced architecture with built in scalability allows customers to target it to many silicon libraries from 0.18 um-0.090 nm ASIC, SOC or FPGA without compromising performance/ functionality.

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Intelop Corporation, a leading IP developer, customization & engineering services provider, today announced their second generation TCP offload engine/TCP accelerator, with integrated Ethernet MAC and AMBA host bus interface running at 2 Gbps sustained rates . It implements control plane and data plane processing of TCP/IP in hardware that is at least 20 times faster than TCP/IP software stack. In addition, it also provides, as an option, a generic bus interface that can be used to integrate other interfaces e.g. PCI-Exp, PCI-X etc. It can be scaled down to 100 Mbps with fewer features also.

“Because of its advanced scalable architecture, it can be customized to implement differentiated features and performance requirements to meet customer’s specifications e.g. Misc. protocols processing module, in addition to TCP/IP, ARP module, customizable number of simultaneous connections, TCP/IP performance tuning based upon type of network/traffic and application usage, scalable packet FIFO size, scalable size of Session Management table, Session Parameters, scalable size of direct store Packet memories, integrated DDR/SSRAM controllers, choice of PHY interface - XGMII or Serial, and many other features,” said Kelly Masood, President of Intelop.

The IP core can be used in the appliances ranging from high-end Network Intrusion Detection/Prevention, Network Security, Content Protection/Security, Network Switches appliances and Routers to low-cost Layer-2/3 switching/routing equipment, servers and NICs.

“We utilized our expertise in designing highly successful and advanced technology Multi-Giga bit Enterprise-class IDS/IPS, Network Security appliances employing SOCs also designed by intelop in defining the architecture of this TOE engine,” said Kelly Masood. Intelop also integrated many of these IPs with other standard blocks in SOCs and developed necessary software as total turn key solutions as well.

“We are excited about this new crown jewel and the ability to develop value-added networking silicon and total solutions for our customers.” said Kevin Moore of Intelop.

Intelop Corporation is a custom IP developer, SoC/ASIC/FPGA integrator and engineering services provider for Networking, Network Security, storage and Embedded Systems. They offer silicon proven semiconductor IP and services with comprehensive hardware and software experience.

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