

## JumpGen Introduces First AdvancedMC Board Using the Tiler TILE64 Processor

The T6M-100 offers the highest performance AdvancedMC™ board with 64 general-purpose cores and two 10 Gbps Ethernet interfaces

**Carlsbad, Calif., Sept. 2, 2008** – JumpGen Systems today announced the T6M-100, an AdvancedMC (PrAMC) platform based on the Tiler® TILE64™ processor that offers 64 cores of general purpose and signal processing compute coupled with over 20 Gbps of full-duplex I/O. The T6M-100 provides the industry's highest compute density in the smallest most energy efficient form factor.

The single-wide T6M-100 is one of several JumpGen PrAMC products that support 10 Gbps Ethernet interfaces addressing growing market requirements for IP networks. The T6M-100 enables equipment manufacturers to dramatically reduce their time-to-market and fulfill the growing demand for converged, content-aware IP networks that accommodate data, voice and video. The board's dual 10Gbps Ethernet interfaces and 64 processor cores make it ideal for hosting high-bandwidth embedded communication applications such as wireless radio network controllers (RNCs), multimedia gateways, media transcoders/transraters, session border controllers, modular servers, firewalls, and deep-packet inspection appliances.

"Using the Tiler TILE64 processor, the T6M-100 offers the highest performance and performance-per-watt of any AMC card on the market today. This further establishes the TILE64 processor's leadership in the embedded market," said Omid Tahernia, CEO and President, Tiler Corporation. "We are very pleased to work with JumpGen and impressed with their execution on this project. AMC board designs are complex, but JumpGen was able to move from initial design to operating prototypes in less than three months."

"We see a growing need for 10Gbps interfaces supporting secure IP communications and video processing in both wireless and wired networks," said Harry White, JumpGen Systems President and CEO. "Adding to our existing accomplishments, the T6M-100 is the first product to offer 64 general purpose processor cores and multiple XAUI interfaces in the small footprint offered by a single-wide AdvancedMC. The Tiler technology enables us to meet the growing market demand for performance and low power. The T6M-100 is already selling with orders placed by a tier-one Telecom Equipment Manufacturer (TEM)."

"JumpGen Systems is a leader in AdvancedMC product development and is expanding the ecosystem for AMC.0 R2.0 and AMC.2 R1.0 compliant products. We are pleased that JumpGen Systems has joined PICMG® as an Executive Member and is developing new products based on many of the PICMG standards," said Joe Pavlat, President of PICMG, the open standards consortium.

The T6M-100 is supported by the Tiler Multicore Development Environment (MDE), an integrated set of tools and libraries that brings simplicity to multicore programming and helps programmers harness the full potential of their processors. The MDE offers full support for C/C++, standard programming paradigms, and the most advanced multicore debugging and optimization tools.

### T6M-100 Features

- TILE64 processor with 64 cores running at 700MHz
- 2GB of ECC DDR2 memory running at 800MHz
- Up to 8GB of persistent memory
- Dual 10GigE interfaces (AMC.2 Type 6 or AMC.2 Type 5 with 10Gbps SFP+ on front panel)
- Dual GigE interfaces (AMC.2 Type E2)
- Front Panel I/O includes an RS-232 Serial and an optional SFP+ for 10Gbps fiber connection
- Available in both full and mid-size AMC configurations for AdvancedTCA® (ATCA), MicroTCA™ and proprietary architecture systems.
- RoHS-compliant
- Complete set of tools and runtime software stack provided by the Tiler Multicore Development Environment

### **The TILE64 processor**

The TILE64 processor is ideally suited for high performance embedded system markets. In the networking and telecommunications areas, the TILE64 processor is designed into switches and security appliances to provide unmatched performance of up to 10 Gbps of L4-L7 services. In the digital video and multimedia market, the TILE64 processor delivers an unprecedented two streams of broadcast-quality, high-definition H.264-encode capability in a single chip and more than ten streams of encode for high-definition video conferencing applications.

### **Availability**

The T6M-100 is shipping to select customers in Q3, 2008 and will be generally available in Q4, 2008.

### **About Tiler**

Tiler Corporation is the industry leader in highly-scalable general purpose multicore TILE™ processor family for the embedded market. The Tiler processors are based on a new mesh iMesh™ architecture that scales to hundreds of RISC-based cores on a single chip. The distributed nature of the Tiler revolutionary architecture and the standards-based tools, such as C/C++ compiler, GNU tools and Eclipse IDE, provide an unprecedented combination of performance, power efficiency and programming flexibility. Tiler was founded in October 2004 and launched its first product, the 64-core TILE64™ processor, in August 2007. The company is headquartered in San Jose, Calif. with locations in Westborough, Mass., Beijing and Bangalore, India.

### **About JumpGen Systems:**

JumpGen Systems is an agile, innovative supplier of embedded computing solutions. JumpGen Systems features an experienced team of engineers to define, develop and deliver critical embedded computing products for our customers. JumpGen's team has a successful track record in bringing new embedded processor solutions to market, quickly and cost-effectively. The JumpGen Systems team has delivered production solutions to embedded customers in industry standard form factors such as AdvancedMC, AdvancedTCA, CompactPCI®, PMC, XMC, and VME form factors as well as custom, proprietary solutions. JumpGen Systems is a privately held company headquartered in Carlsbad, California. JumpGen Systems is an Executive Member of PICMG®. For more information, visit [www.JumpGen.com](http://www.JumpGen.com)

### **JumpGen Systems Contact:**

Greg Pause, Director Business Development  
Telephone: 760-931-7800  
Email: [gpause@JumpGen.com](mailto:gpause@JumpGen.com)